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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,660,378
DATED : February 9, 2010
INVENTOR(S) : Jean-Marc BROSSIER et al.

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Abstract section on front cover of patent from

“A method for estimating the phase in a digital communication system, comprising the steps of:
receiving and storing a block of observations Y_{subk} ;
executing at least one phase locked loop (PLL) on a pre-determined sequence of observations extracted from said block.

Each PLL provides an intermediate value, combining both intermediate values makes it possible to provide a sharpened phase estimate. The method allows realization of an improved phase estimator based on the simplicity of phase locked loops.”

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MAILING ADDRESS OF SENDER:USPTO Customer Number: 23334 PATENT NO. 7,660,378

FLEIT GIBBONS GUTMAN
BONGINI & BIANCO P.L.
One Boca Commerce Center
551 N.W. 77th Street,Suite 111
Boca Raton, Florida 33487



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